

ABSTRACT

A communications interface apparatus includes a register, first and second memories, and a control circuit. The register circuit stores data to be transferred to a host computer. The first memory stores first information indicating a specific address of the register and representing an access to the apparatus executed by the host computer for a data transfer. The second memory stores second information, sent from the host computer in association with the first information stored in the first memory, to be written into the register at the specific address indicated by the first information stored in the first memory. The control circuit performs an information writing operation for writing the first information into the first memory and the second information into the second memory in chronological order of accesses executed.